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Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450

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Karen Cinq-Mars

(Signature)

9/19/03

(Date)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of : September 18, 2003
Hsu et al. :
Serial No. 10/604,799 : Examiner:
Filed: 08-18-03 : IBM Corporation
Dept. 18G/Bldg. 300-482
2070 Route 52
Hopewell Junction,
New York 12533-6531
Title: APPARATUS AND METHOD FOR
DETECTING LOSS OF HIGH-SPEED
SIGNAL :

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in
37 C.F.R. § 1.56, and further pursuant to the provisions of
37 C.F.R. §§ 1.97 and 1.98, applicants hereby respectfully submit
copies of the non-US patents and publications as listed on Form
PTO-1449, attached hereto.

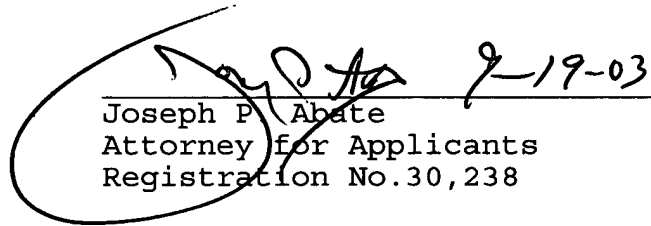
In citing these documents, no representation is made nor
intended as to the pertinency or non-pertinency of the art, that
better art than that listed is not available, or that other art is
not applicable.

No fee is believed to be due for this submission. If any fees

FIS920030121US1
App. No. 10/604,799

are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,



Joseph P. Abate
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INTERNATIONAL BUSINESS MACHINES CORPORATION
Intellectual Property Law Department
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FORM PTO-1449 (Modified)

ATTY. DOCKET NO.

SERIAL NO.

FIS920030121US1

10/604,799

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENTAPPLICANT:
Hsu et al.

(Use several sheets if necessary)

FILING DATE:
08-18-03

GROUP:

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	ISSUED DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AJ							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	"A Novel High Speed CMOS Signal Transition Detector Circuit," <u>Research Disclosure</u> , April 16, 2001, IPCOM000014372D
	AO	
	AP	
	AQ	
	AR	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.